

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY
Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I SECOND SEMESTER VLSI AND EMBEDDED SYSTEMS AT REGULAR EXAM HELD DURING APRIL/MAY 2026

					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark		
Semester	Student Name	Course Code			ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECCS130 T	ECVL178 T										
					3.00	3.00	3.00	2.00	3.00	3.00	3.00										
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SPEECH PROCESSING AND APPLICATIONS (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)										
Semester : 2 JEEL TUSHAR SHAH					AA	AB	AA	AA	AB	AA	AB										
P25VL001 20.00 20.00 9.55 9.55																					
Semester : 2 SAUMYA JIGNESH JOSHI					BB	BC	AB	BB	CD	BB	BB										
P25VL002 20.00 20.00 7.55 7.83																					
Semester	Student Name	Course Code			ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECVL118 T	ECVL178 T										
					3.00	3.00	3.00	2.00	3.00	3.00	3.00										
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SOC DESIGN (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)										
Semester : 2 MANOGYA SHARMA					BB	CC	BC	AA	BC	BC	AB										
P25VL003 20.00 20.00 7.60 7.78																					
Semester : 2 MANISH GALBARAM SUTHAR					BB	BB	AA	AA	AA	AB	BB										
P25VL004 20.00 20.00 8.95 9.10																					
Semester : 2 KONDA NIHAR					AB	BC	AB	AB	BC	AA	AB										
P25VL005 20.00 20.00 8.55 8.40																					
Semester	Student Name	Course Code			ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECCS130 T	ECVL178 T										
					3.00	3.00	3.00	2.00	3.00	3.00	3.00										
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SPEECH PROCESSING AND APPLICATIONS (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)										
Semester : 2 VALMIKI SAMPADHA					FF	BC	BB	AB	CD	DD	BB										
P25VL006 20.00 17.00 5.70 6.24																					BACKLOG : 3 CREDIT(S)

Abbreviations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFACTORY ATTENDANCE NP/NF-AUDIT PASS/FAIL

Date : 08-06-2026

The actual mark statement with the Academic / Examination section shall be final & binding to all

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		1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name	ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECVL118 T	ECVL178 T								
	Course Code															
	Credit	3.00	3.00	3.00	2.00	3.00	3.00	3.00								
Admission Number	Reg-Credit Earn-Credit SGPA CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SOC DESIGN (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)								
Semester :	2 MAHESHWAR REDDY	BB	DD	AB	AB	BB	BB	BB								
	P25VL007 20.00 20.00 7.65 7.73															
Semester :	2 PRACHI PRAKHAR	BB	BB	BB	AB	BC	AB	AB								
	P25VL008 20.00 20.00 8.25 7.40															
Semester :	2 KOMBATTULA ROHIT	BC	FF	CC	AB	CC	BC	CC								BACKLOG : 3 CREDIT(S)
	P25VL010 20.00 17.00 5.70 6.11															
Semester :	2 CHENNAMRAJU SHANMUKHA ANUSHITH VARMA	BB	CC	BB	AA	CC	BB	BB								
	P25VL011 20.00 20.00 7.60 7.68															
Semester :	2 JAMSHEER M	AB	CC	BB	AB	BB	BB	AB								
	P25VL012 20.00 20.00 8.10 8.48															
Semester :	2 HANUMANTHARAO THOLLAMADUGU	BB	DD	BC	BB	BC	BC	BC								
	P25VL013 20.00 20.00 6.80 7.25															
Semester :	2 DHAIRYA DIPAKKUMAR RUPANI	AB	BC	AB	AA	BB	BB	AB								
	P25VL014 20.00 20.00 8.50 8.53															
Semester :	2 SOURABH KUMAR	BC	CD	AA	BB	BC	CC	BC								
	P25VL015 20.00 20.00 7.10 7.53															
Semester	Student Name	ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECCS130 T	ECVL178 T								
	Course Code															
	Credit	3.00	3.00	3.00	2.00	3.00	3.00	3.00								
Admission Number	Reg-Credit Earn-Credit SGPA CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SPEECH PROCESSING AND APPLICATIONS (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)								
Semester :	2 HEMASREE BUKKE	AB	BC	AB	AB	DD	BC	BB								
	P25VL016 20.00 20.00 7.50 6.70															

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					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark	
Semester	Student Name	Course Code			ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECVL118 T	ECVL178 T									
					3.00	3.00	3.00	2.00	3.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SOC DESIGN (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)									
Semester : 2 MARDA ADHITHYA					BC	BC	BC	AB	BC	AB	AB									
P25VL017 20.00 20.00 7.80 7.65																				
Semester	Student Name	Course Code			ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECCS130 T	ECVL178 T									
					3.00	3.00	3.00	2.00	3.00	3.00	3.00									
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SPEECH PROCESSING AND APPLICATIONS (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)									
Semester : 2 GOPI KRISHNA THUMMALA					BC	FF	CD	AB	FF	CD	BC									
P25VL018 20.00 14.00 4.50 5.94																				BACKLOG : 6 CREDIT(S)

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		1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name	Course Code	ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECVL118 T	ECVL178 T							
		Credit	3.00	3.00	3.00	2.00	3.00	3.00	3.00							
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SOC DESIGN (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)					
Semester : 2 LOCHAN KORADA			AB	CD	BC	BB	BB	AB	BC							
P25VL019 20.00 20.00 7.55 7.40																
Semester : 2 PRADEEP PANDURU			AB	BC	BC	AB	BB	AA	AA							
P25VL020 20.00 20.00 8.55 8.50																
Semester : 2 ABHISHEK B V			BB	BC	BC	AA	CC	BC	BC							
P25VL021 20.00 20.00 7.30 7.48																
Semester : 2 SIVAKUMAR CHAKRAPALLI			AB	CC	BB	BB	BC	AA	AB							
P25VL022 20.00 20.00 8.15 7.93																
Semester : 2 GANGANABOINA JAYA VENKATA SIVA			BB	FF	CC	BC	FF	CC	BC							BACKLOG : 6 CREDIT(S)
P25VL023 20.00 14.00 4.75 6.79																
Semester : 2 SYAM JOSEPH			BC	CD	CC	AB	BC	CC	BB							
P25VL024 20.00 20.00 6.75 6.93																
Semester : 2 ROHITH RAJU CHAKRAVARAM			AA	CC	BC	AB	BC	AB	BB							
P25VL025 20.00 20.00 7.95 7.48																
Semester : 2 RUDRA PRATAP MAHATO			AB	BB	BB	AA	AB	BC	AB							
P25VL026 20.00 20.00 8.50 8.45																
Semester	Student Name	Course Code	ECVL102 T	ECVL104 T	ECVL106 P	ECVL108 P	ECVL116 T	ECCS130 T	ECVL178 T							
		Credit	3.00	3.00	3.00	2.00	3.00	3.00	3.00							
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	ANALOG VLSI DESIGN	REAL-TIME SYSTEMS	VLSI LAB - II	MINI PROJECT	VLSI SYSTEM DESIGN (CE-IV)	SPEECH PROCESSING AND APPLICATIONS (CE-V)	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (EIS-I)					
Semester : 2 PARMAR KALPESHKUMAR MANGALBHAI			AA	AB	AA	AB	AA	AB	AA							
P25VL027 20.00 20.00 9.60 9.53																

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Semester :	2 ABHISHEK BISHT	BB	BC	CC	AB	FF	BC	BB								BACKLOG : 3 CREDIT(S)
	P25VL029 20.00 17.00 6.30 6.65															
Semester :	2 KRISHNAKUMARI PATEL	BC	FF	DD	BC	FF	FF	DD								BACKLOG : 12 CREDIT(S)
	P25VL030 20.00 11.00 2.95 5.00															
Semester :	2 KARAN WADHWANI	AB	BB	BB	AA	BB	BB	BB								
	P25VL031 20.00 20.00 8.35 8.03															

Total Student Count : 29